1 T	Hits	Search Text	DB	Time stamp
Number	11203			
31	4	graphics and ((level adj3 ("2" or two))	USPAT;	2004/03/02
		adj5 cache) and register and memory and control\$3 and samples and pixels and	US-PGPUB; EPO; JPO	18:16
		(blocks or tiles) and fifo and ram and	10, 010	
		alu and @ad<20020228		
32	4	graphics and ((level adj3 ("2" or two))	USPAT;	2004/03/02
		adj5 cache) and register and memory and control\$3 and samples and pixels and	US-PGPUB; EPO; JPO	18:18
		(blocks or tiles) and fifo and ram and		
	505	alu and @ad<20020228		0004/00/00
33	525	graphics and ((level adj3 ("2" or two)) adj5 cache) and register and memory and	USPAT; US-PGPUB;	2004/03/02
		@ad<20020228	EPO; JPO	10.51
34	30	]	USPAT;	2004/03/02
		adj5 cache) same RAM) and ((level adj ("1" or one)) adj5 cache) same ((level	US-PGPUB; EPO; JPO	18:37
		adj3 ("2" or two)) adj5 cache) and	1 110, 010	
		register and memory and @ad<20020228		
35	23	(graphics and (((level adj3 ("2" or two)) adj5 cache) same RAM) and ((level adj	USPAT; US-PGPUB;	2004/03/02
		("1" or one)) adj5 cache) same ((level	EPO; JPO	10.43
		adj3 ("2" or two)) adj5 cache) and		
		register and memory and @ad<20020228) not		
36	0	sun not lavelle 20020129220.URPN.	USPAT	2004/03/02
				18:40
37	0	((graphics and (((level adj3 ("2" or two)) adj5 cache) same RAM) and ((level	USPAT; US-PGPUB;	2004/03/02
		adj ("1" or one)) adj5 cache) same	EPO; JPO	10:43
		((level adj3 ("2" or two)) adj5 cache)	,	
		and register and memory and @ad<20020228) not sun not lavelle) and (memory adj3		
		request adj7 processor)		
38	21	(graphics and ((level adj3 ("2" or two))	USPAT;	2004/03/02
		adj5 cache) and register and memory and @ad<20020228) and (memory adj3 request	US-PGPUB; EPO; JPO	18:43
		adi7 processor)	EPO, JPO	
39	16	((graphics and ((level adj3 ("2" or two))	USPAT;	2004/03/02
	·	adj5 cache) and register and memory and @ad<20020228) and (memory adj3 request	US-PGPUB; EPO; JPO	18:44
		adj7 processor)) not sun not lavelle	EPO, JPO	
40	12	(((graphics and ((level adj3 ("2" or	USPAT;	2004/03/02
		two)) adj5 cache) and register and memory and @ad<20020228) and (memory adj3	US-PGPUB;	18:44
		and @ad<20020228) and (memory adj3   request adj7 processor)) not sun not	EPO; JPO	
		lavelle) and "11 cache"		
41	0	(((graphics and ((level adj3 ("2" or two)) adj5 cache) and register and memory	USPAT;	2004/03/02
		and @ad<20020228) and (memory adj3	US-PGPUB; EPO; JPO	10.44
		request adj7 processor)) not sun not	,	
42	12	lavelle) and "13 cache" (((graphics and ((level adj3 ("2" or	IISDAT.	2004/03/02
176	12	two)) adj5 cache) and register and memory	USPAT; US-PGPUB;	18:45
		and @ad<20020228) and (memory adj3	EPO; JPO	
		request adj7 processor)) not sun not lavelle) and (multiple adj5 cache)		
43	8		USPAT;	2004/03/02
		two)) adj5 cache) and register and memory	US-PGPUB;	18:45
		and @ad<20020228) and (memory adj3 request adj7 processor)) not sun not	EPO; JPO	
		lavelle) and (multiple adj5 cache) and		
		ram and registers		
44	7	(((())================================	USPAT;	2004/03/02
		two)) adj5 cache) and register and memory and @ad<20020228) and (memory adj3	US-PGPUB; EPO; JPO	18:46
		request adj7 processor)) not sun not		
		lavelle) and (multiple adj5 cache) and		
		ram and registers) and (modif\$4 same cache)		

•				
45 .	7	((((graphics and ((level adj3 ("2" or two)) adj5 cache) and register and memory and @ad<20020228) and (memory adj3 request adj7 processor)) not sun not lavelle) and (multiple adj5 cache) and ram and registers) and (modif\$4 same cache) and graphics	USPAT; US-PGPUB; EPO; JPO	2004/03/02 18:46
46	8		USPAT; US-PGPUB; EPO; JPO	2004/03/02 18:58
47	0	gargan same same gargan	USPAT; US-PGPUB; EPO; JPO	2004/03/02 18:59
48	100	("13" adj3 cache) and (modif\$4 or change or status same cache) and graphics and @ad<20020228	USPAT; US-PGPUB; EPO; JPO	2004/03/02 18:59
49	79	(("13" adj3 cache) and (modif\$4 or change or status same cache) and graphics and @ad<20020228) not sun not lavelle	USPAT; US-PGPUB; EPO; JPO	2004/03/02 18:59